

Have you ever asked yourself how to interpret the requirements of ISO 26262 for your IC or IP development project?

Are you overwhelmed by the complexity of your SoC safety analyses?

Have you ever wondered how many safety mechanisms are enough?

Do you want to know what is new in the 2nd edition of ISO 26262, and what it means to you?

Join our training and learn more about these and other interesting topics regarding ISO 26262.





DE0208 Semiconductor IC and IP Development according to ISO 26262

Content:

- 4-day training on ISO 26262 compliant system and hardware development, with focus on the specifics for the semiconductor industry
- Addressing requirements, challenges and methods for functional safety management throughout all phases of the safety life cycle
- Introducing ISO 26262 item definition, concept phase and system development to provide an overview understanding of how requirements for safety related ICs and IPs are derived by OEMs and Tier1 suppliers

Detailing the specification of safety requirements and safety concepts

Addressing hardware development requirements and quantitative analyses necessary to show compliance with the target metrics.

Comparing ISO 26262 process requirements against state-of-the-art automotive semiconductor development processes, and deriving the required adjustments and method for ISO 26262 compliance

Language:	German or English, training material will be in English
Duration:	4 days
Location:	exida.com GmbH office Prof. Messerschmitt Straße 1 85579 Neubiberg / Germany
Certificate:	Each participant gets a letter of attendance.
	At the end of the 4 th day there is a possibility to do the FSP exam. This test is optional and free of charge.





DE0208 Semiconductor IC and IP Development according to ISO 26262

Who should attend?

- Project Managers
- Safety Managers
- Concept Engineers
- Design and Verification Engineers (Hardware, System)
- Application and Field Application Engineers
- Quality managers
- Project Team and Automotive Business Group Leaders



For more information, please contact:

Kerstin Tietel

+49 89 4411 8232

kerstin.tietel@exida.com





DE0208 Semiconductor IC and IP Development

according to ISO 26262

Agenda

General Functional Safety Management

- Overview of Functional Safety and ISO 26262
- Functional Safety Management
- Automotive Safety Lifecycle
- Safety Lifecycle Tailoring for IC and IP Development Projects
- Safety Plan and Safety Case
- Confirmation Measures and Verification Reviews
- Functional Safety Assessment and Certification
- Supporting Processes: Configuration and Change Management

Item Definition and Concept Phase

- o Item Definition
- Hazard Analysis & Risk Assessment
- Functional Safety Concept and Functional Safety Requirements
- Safety Element out of Context (SEooC) Definition
- Supporting Processes: Specification and Management of Safety Requirement

System Development

- Technical Safety Concept and Technical Safety Requirements
- o ASIL Decomposition
- Dependent Failure Analysis (DFA)
- Hardware-Software Interface Specification
- System Integration and Testing, Validation

IC and IP Hardware Development

- o Hardware Safety Requirements
- Hardware Safety Architecture and Design Specification
- Safety Functions and Safety Mechanisms for Semiconductors
- Hardware Design Implementation and Verification, and Special Requirements for Semiconductor Development Projects
- o Safety Manual
- Supporting processes: Confidence in the Use of Software Tools





DE0208 Semiconductor IC and IP Development

according to ISO 26262

Safety Analyses

- o Overview of Safety Analyses
- o Qualitative and Quantitative Fault Tree Analysis
- Failure Modes, Effects and Diagnostics Analysis (FMEDA)
- Failure Rates Estimation
- Failure Modes and Failure Mode Distribution
- Estimation and Verification of Diagnostic Coverage
- Relative and Absolute Metrics (SPFM, LFM, PMHF)



